

## **Amendment to the Claims**

Please cancel claims 3-5 and 9, and amend claims 1, 2, 7, 8 and 10 as shown in the following listing of claims. This listing of claims will replace all prior versions, and  
5 listings, of claims in the application.

1. (currently amended) Modulator for generating a digital I/Q signal having a plurality of time-slots, the modulator comprising means for introducing a dip in an envelope of the digital I/Q signal in a guard interval between adjacent time-slots of the plurality of time-slots, wherein the means for introducing the dip in the envelope of the digital I/Q signal in the guard interval between the adjacent time-slots of the plurality of time-slots comprises a digital multiplier for multiplying at least one of the I signal and the Q signal of the I/Q signal with a dip-shaped waveform.

2. (currently amended) Modulator according to claim 1, wherein the means for introducing the dip in the envelope of the digital I/Q signal in the guard interval between adjacent time-slots of the plurality of time-slots comprises another digital multiplier, the digital multiplier being used to multiply the I signal with the dip-shaped waveform, the another digital multiplier being used to multiply the Q signal with the dip-shaped waveform a digital multiplier for multiplying the I signal and the Q signal of the I/Q signal with a dip-shaped waveform.

3. (canceled).

4. (canceled).

5. (canceled).

6. (original) Modulator in accordance with claim 1, wherein the modulator is a GMSK modulator and a 8PSK modulator.

7. (currently amended) Signal processing method for generating a digital I/Q signal having a plurality of time-slots, the signal processing method comprising the steps of:

(a) modulating the I signal and the Q signal for generating the I/Q signal; and

(b) introducing a dip in an envelope of the digital I/Q signal in a guard interval between adjacent time-slots of the plurality of time-slots,

wherein the introducing includes multiplying at least one of the I signal and the Q signal of the I/Q signal with a dip-shaped waveform.

8. (currently amended) Signal processing method according to claim 7, wherein the introducing includes multiplying each of the I signal and the Q signal of the I/Q signal with the dip-shaped waveform further comprising the step of multiplying the I signal and the Q signal of the I/Q signal with a dip-shaped waveform.

9. (canceled).

10. (currently amended) Computer-readable medium embodying a program of instructions to perform a method Computer program for generating a digital I/Q signal having a plurality of time-slots, in particular for a chipset for implementing a TDMA transmitter in a GSM-, EDGE- or EGPRS-system, the method computer program comprising the steps of:

(a) modulating the I signal and the Q signal for generating the I/Q signal; and

(b) introducing a dip in an envelope of the digital I/Q signal in a guard interval between adjacent time-slots of the plurality of time-slots,

wherein the introducing includes multiplying at least one of the I signal and the Q signal of the I/Q signal with a dip-shaped waveform.

11. (original) Transmitter comprising a modulator according to claim 1.